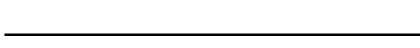
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NP110N04PUG

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The NP110N04PUG is N-channel MOS Field Effect
Transistor designed for high current switching applications.

ORDERING INFORMATION

PART NUMBER	PACKAGE
NP110N04PUG	TO-263 (MP-25ZP)

FEATURES

- Channel temperature 175 degree rating
- Super low on-state resistance $R_{DS(on)}$ = 1.8 m Ω MAX. (Vgs = 10 V, ID = 55 A)

• Low Ciss: Ciss = 17100 pF TYP.

(TO-263)



ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (V _{GS} = 0 V)	Voss	40	V
Gate to Source Voltage (V _{DS} = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±110	Α
Drain Current (pulse) Note1	$I_{D(pulse)}$	±440	Α
Total Power Dissipation (T _A = 25°C)	P _{T1}	1.8	W
Total Power Dissipation (Tc = 25°C)	P _{T2}	288	W
Channel Temperature	Tch	175	°C
Storage Temperature	T _{stg}	-55 to +175	°C
Repetitive Avalanche Current Note2	Iar	72	Α
Repetitive Avalanche Energy Note2	Ear	518	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Tch \leq 150°C, VDD = 20 V, Rg = 25 Ω , Vgs = 20 \rightarrow 0 V

THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	0.52	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

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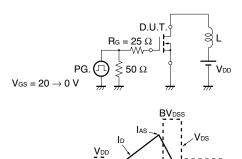


ELECTRICAL CHARACTERISTICS (TA = 25°C)

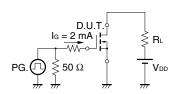
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 40 V, V _{GS} = 0 V			1	μА
Gate Leakage Current	Igss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate to Source Threshold Voltage Note	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 55 A	45	89		S
Drain to Source On-state Resistance Note	RDS(on)	V _{GS} = 10 V, I _D = 55 A		1.4	1.8	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V		17100	25700	pF
Output Capacitance	Coss	V _{GS} = 0 V		1420	2130	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		890	1610	pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 20 V, I _D = 55 A		54	120	ns
Rise Time	tr	V _{GS} = 10 V		140	350	ns
Turn-off Delay Time	td(off)	$R_G = 0 \Omega$		130	260	ns
Fall Time	tf			21	60	ns
Total Gate Charge	QG	V _{DD} = 32 V		260	390	nC
Gate to Source Charge	Qgs	V _{GS} = 10 V		57		nC
Gate to Drain Charge	Q _{GD}	I _D = 110 A		83		nC
Body Diode Forward Voltage Note	V _F (S-D)	IF = 110 A, VGS = 0 V		0.9	1.5	V
Reverse Recovery Time	trr	I _F = 110 A, V _{GS} = 0 V		60		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		80		nC

Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

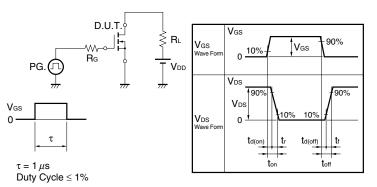






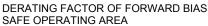
Starting Tch

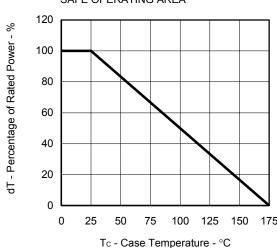
TEST CIRCUIT 2 SWITCHING TIME



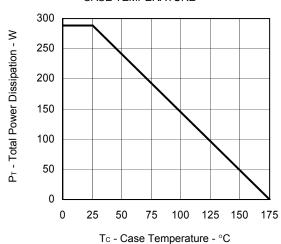


TYPICAL CHARACTERISTICS (TA = 25°C)

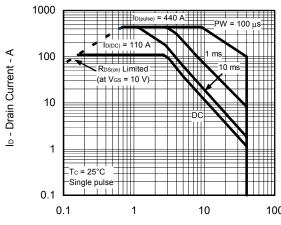


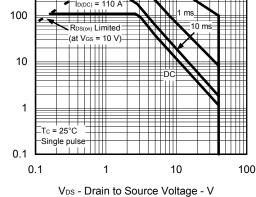


TOTAL POWER DISSIPATION vs. CASE TEMPERATURE

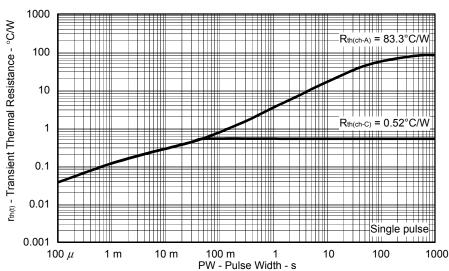


FORWARD BIAS SAFE OPERATING AREA





TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



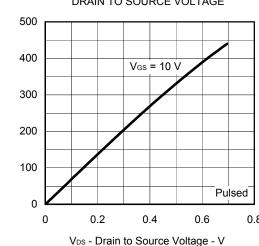
3 Data Sheet D16852EJ1V0DS



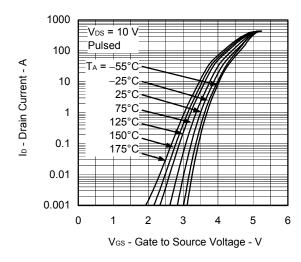
Ip - Drain Current - A

Ves(th) - Gate to Source Threshold Voltage - V

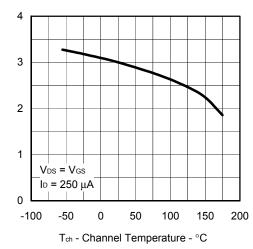
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



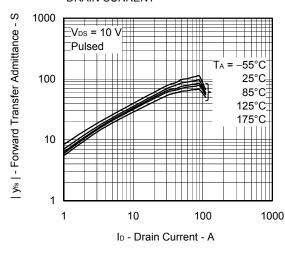
FORWARD TRANSFER CHARACTERISTICS



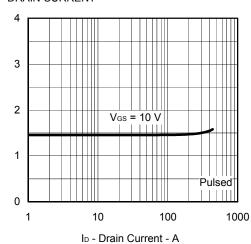
GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE



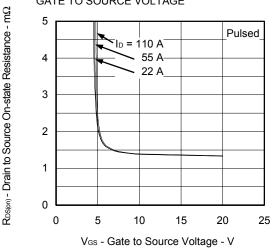
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

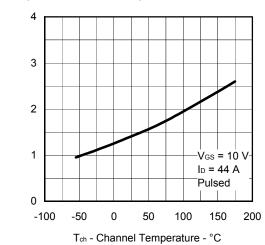


R_{DS(on)} - Drain to Source On-state Resistance - mΩ

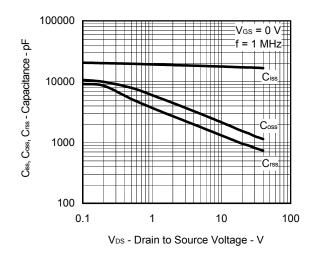


RDS(on) - Drain to Source On-state Resistance - m\Omega

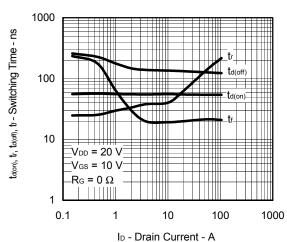
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



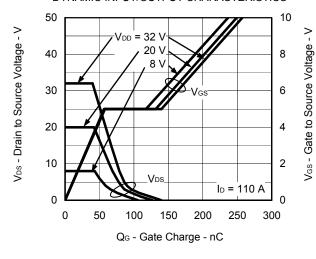
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



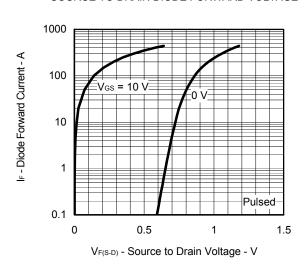
SWITCHING CHARACTERISTICS



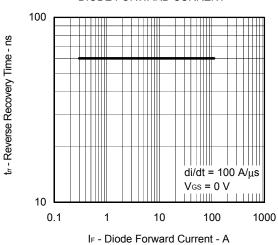
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE

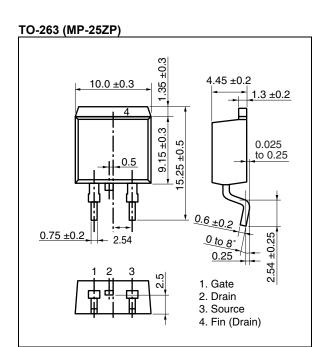


REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT

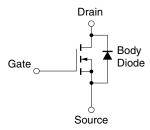




PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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