

ZONE	REV	REASON DESCRIPTION	DATE	DRAWN	APP'VD	DATE
	01	Initial Release per DCN 12960	1/3/01	DT	DT	10/4/01
	02	Change per DCN 13318	7/12/01	DT	DT	11/29/01
	03	Change per DCN 13645	11/29/01	DT	DT	11/30/01
	04	Change per DCN 14229	9/17/02	DT	DT	9/17/02

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TITLE:

**Tray, Jedec L/P FC-BGA
40mm X 40mm 2.70T**

TOLERANCES



.X .XX .XXX

Unless otherwise specified, dimensions are in inches.

SIZ
E

A

DRAWING NUMBER

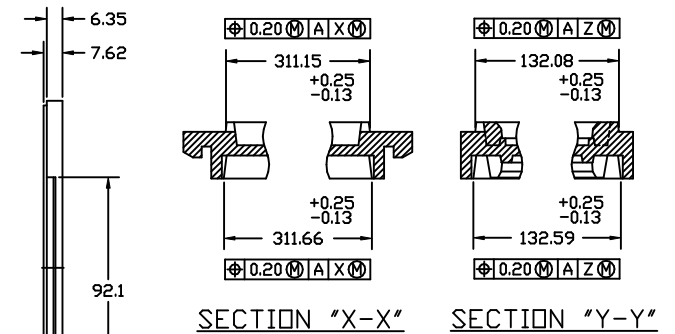
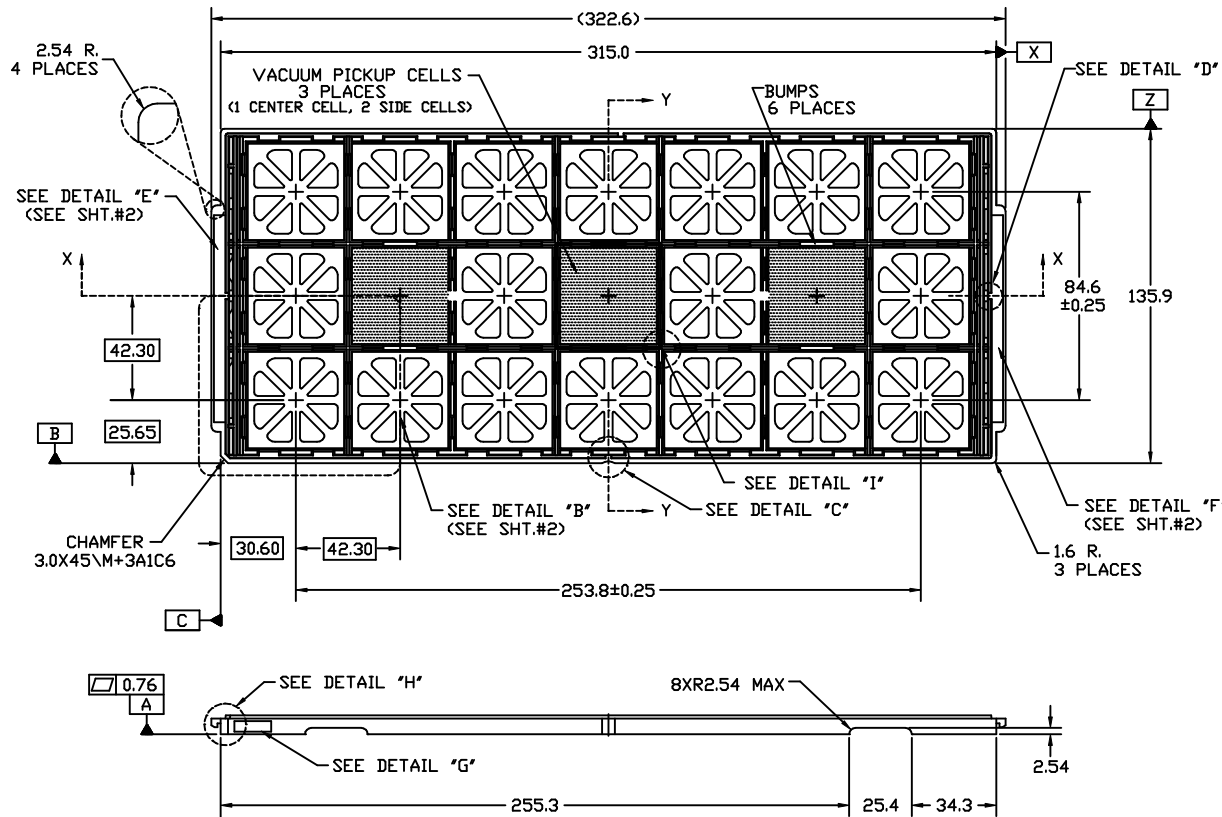
SIT0084

REV.

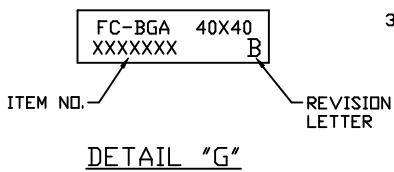
04

SCALE

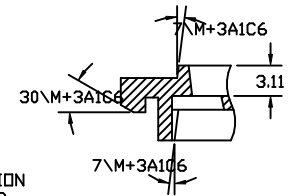
SHEET 0 OF 2



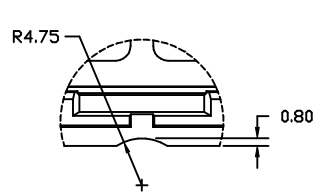
TRAY STACKING DETAIL
SCALE : 3/1



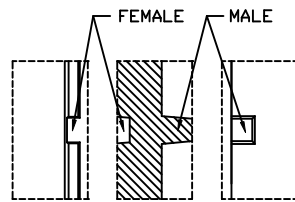
DETAIL "G"



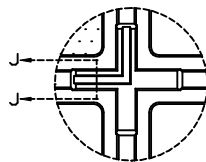
DETAIL "H"
SCALE : 4/1



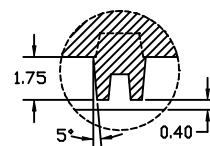
DETAIL "C"
SCALE : 4/1



SLIP-LOCK DETAILS
DETAIL "D"
SCALE : 4/1



BOTTOM VIEW
DETAIL "I"
SCALE : 4/1

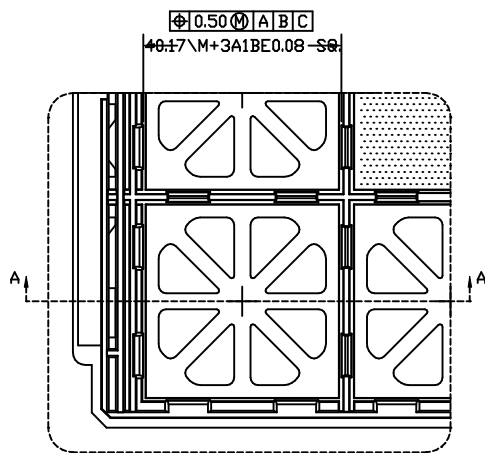


SECTION J-J
VACUUM WALL
SCALE : 10/1

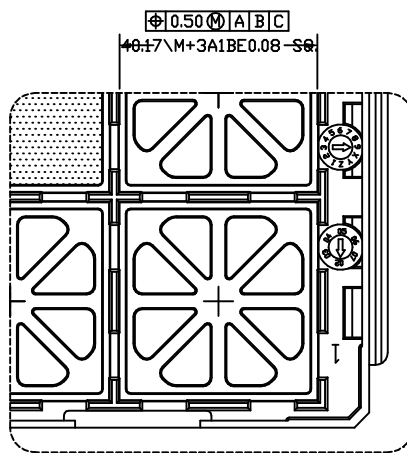
NOTE 1. ESD-SURFACE RESISTIVITY SHALL BE >1X10⁴,
<1X10¹⁰ OHMS/SQUARE PER EIA, JEDEC, ACH
TRAY SPECIFICATIONS.

2. CONFORM TO EIA-541

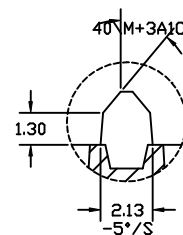
UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN MM STANDARD TOLERANCES IN: DECIMALS X ± 0.25 XX ± 0.13 XXX ± 0.13 ANGULAR ±1°	DESIGNED BY Daewon	DATE 10/24/00	XILINX ®
	DRAWN David Tan	DATE 09/10/02	
BAKEABLE, MPPD BLACK, 150°C MAX Daewon Part No: 12Y-4040-419 Rev. B Daewon Dwg. No: JFCL-4040-2.70T	MATRIX 7x3=21 pockets	TITLE TRAY, JEDEC L/P FC-BGA 40mm X 40mm 2.70T	SIZE A
		DWG NO SIT0084	REV 04
		SCALE NTS	SHEET 1 OF 2



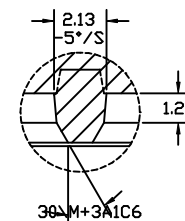
TOP VIEW
DETAIL "B"
SCALE : 2/1
(FROM SHT.#1)



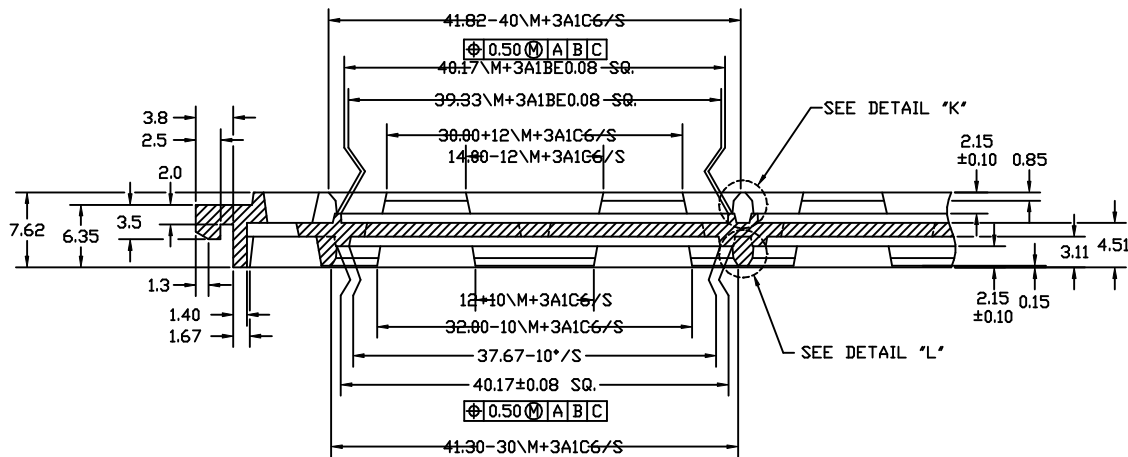
BOTTOM VIEW
DETAIL "B"
SCALE : 2/1
(FROM SHT.#1)



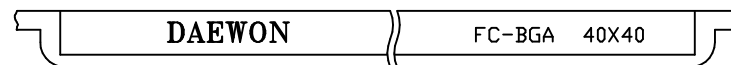
DETAIL "K"
SCALE : 10/1



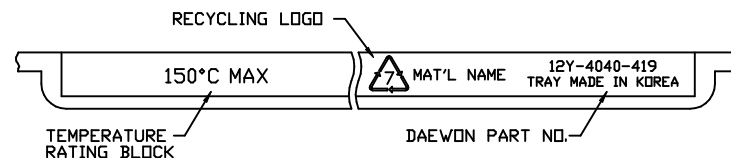
DETAIL "L"
SCALE : 10/1



SECTION "A-A"
SCALE : 4/1



DETAIL "E"
(FROM SHT.#1)



DETAIL "F"
(FROM SHT.#1)

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN MM STANDARD TOLERANCES IN: DECIMALS X ± 0.25 XX ± 0.13 XXX ± 0.13 ANGULAR ±1°	DESIGNED BY	DATE
	DRAWN	DATE
	MATRIX	7x3=21 pockets
BAKEABLE, MPPD BLACK, 150°C MAX Daewon Part No: 12Y-4040-419 Rev. B Daewon Dwg. No: JFCL-4040-2.70T		

TITLE TRAY, JEDEC L/P FC-BGA 40mm X 40mm 2.70T		
SIZE	DWG NO	REV
A	SIT0084	04
SCALE	NTS	SHEET 2 OF 2