

Vishay General Semiconductor

**HALOGEN** 

FREE

# **Surface Mount Trench MOS Barrier Schottky Rectifier**



**DO-214AC (SMA)** 

PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	2.0 A		
$V_{RRM}$	100 V		
I <sub>FSM</sub>	60 A		
V <sub>F</sub> at I <sub>F</sub> = 2.0 A	0.56 V		
T <sub>J</sub> max.	150 °C		
Package	DO-214AC (SMA)		
Diode variation Single die			

### **FEATURES**

- Low profile package
- · Ideal for automated placement
- Trench MOS Schottky technology
- · Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

### **MECHANICAL DATA**

Case: DO-214AC (SMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant and AEC-Q101 gualified

Base P/NHM3\_X - halogen-free, RoHS-compliant and AEC-Q101 gualified

("\_X" denotes revision code e.g. A, B,....)

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes the cathode end

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSSA210	UNIT	
Device marking code		V2B		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	V	
Maximum DC forward assurant	I <sub>F</sub> <sup>(1)</sup>	2.0	Α	
Maximum DC forward current	I <sub>F</sub> <sup>(2)</sup>	1.7		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	60	А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

### Notes

- (1) Mounted on 8 mm x 8 mm pad areas, 1 oz. FR4 PCB
- (2) Free air, mounted on recommended copper pad area



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	1 004	T <sub>A</sub> = 25 °C	V <sub>E</sub> <sup>(1)</sup>	0.61	0.70	V
	$I_F = 2.0 \text{ A}$	T <sub>A</sub> = 125 °C	7 VF (''	0.56	0.65	
Reverse current	V <sub>R</sub> = 70 V	T <sub>A</sub> = 25 °C		1.0	-	μA
		T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	0.95	-	mA
	V 100 V	T <sub>A</sub> = 25 °C	] <sup>IR (-)</sup> [	3.5	150	μA
	$V_{R} = 100 \text{ V}$	T <sub>A</sub> = 125 °C	7 [	2.2	15	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	175	1	pF

#### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER SYMBOL VSSA210				
Typical thermal registance	R <sub>θJA</sub> <sup>(1)</sup>	135	°C/W	
Typical thermal resistance	R <sub>0JM</sub> (2)	25	C/VV	

#### **Notes**

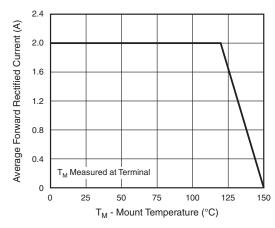
- $^{(1)}$  Free air, mounted on recommended PCB 1 oz. pad area; thermal resistance  $R_{\theta JA}$  junction to ambient
- Units mounted on PCB with 8 mm x 8 mm copper pad areas;  $R_{\theta JM}$  junction to mount

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
VSSA210-M3/61T	0.064	61T	1800	7" diameter plastic tape and reel
VSSA210-M3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel
VSSA210HM3/61T (1)	0.064	61T	1800	7" diameter plastic tape and reel
VSSA210HM3/5AT (1)	0.064	5AT	7500	13" diameter plastic tape and reel
VSSA210HM3_A/H (1)	0.064	Н	1800	7" diameter plastic tape and reel
VSSA210HM3_A/I (1)	0.064	I	7500	13" diameter plastic tape and reel

### Note

(1) AEC-Q101 qualified

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)





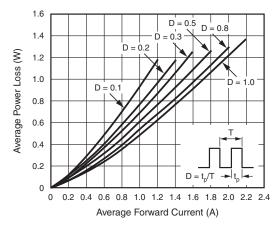


Fig. 2 - Forward Power Loss Characteristics

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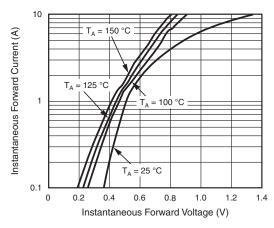


Fig. 3 - Typical Instantaneous Forward Characteristics

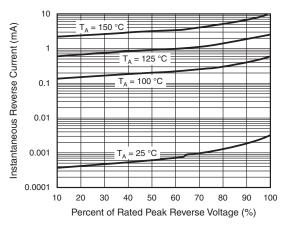


Fig. 4 - Typical Reverse Characteristics

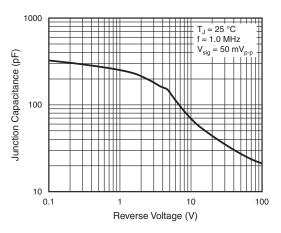


Fig. 5 - Typical Junction Capacitance

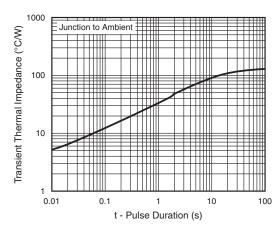
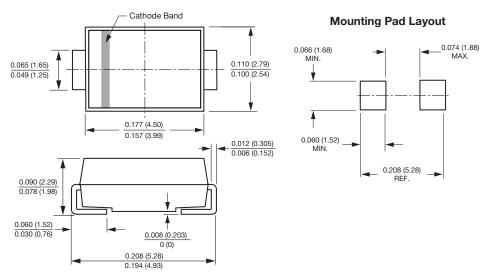


Fig. 6 - Typical Transient Thermal Impedance

## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

## **DO-214AC (SMA)**





## **Legal Disclaimer Notice**

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